

# 2023-32864 SiC Process Engineering - Defectivity M/F

## General information

Job level 40 - Experienced

## Position description

Regions	Europe
Country	Italy
States	Catania
Posting title	SiC Process Engineering - Defectivity M/F
Regular/Temporary	Regular
Job description	<p>You will be a key contributor to SiC Technology volume ramp-up in ST. The work location will be in the new mass-production fab in Catania for the manufacturing of SiC substrate wafers, this facility will be a first of a kind in Europe for the production in volume of SiC epitaxial substrates, integrating all steps in the production flow.</p> <p>Strong link is maintained with STMicroelectronics Silicon Carbide AB, located in Norrköping, Sweden, with opportunities for on-site training and collaboration. STMicroelectronics Silicon Carbide AB, previously named Norstel AB, is the ST competence center in SiC material technology: the company has been developing silicon carbide for 20 years, first at Linköping University, and since 2006 in a dedicated production facility in Norrköping.</p>

Your role will be part of the defectivity group inside the Process Engineering team.

The focus of the group will be to ensure a smooth transition between the R&D activities and the mass-production fab collecting and analyzing all the information required to guarantee the high quality of the final product.

Your main focus will be in Defectivity with the following tasks:

- Lead the process steps transfer from the R&D fab to the High-Volume Manufacturing fab
- Hands-on work with processes and tools used in SiC semiconductor technology
- Conduct systematic troubleshooting of equipment-related problems
- Implement and refine procedures and qualification plans for bringing new processes on line
- Qualify new production tools with suppliers and against internal criteria
- Train new employees (operators, technicians and engineers)
- Ensure process robustness and stability by managing equipment control charts, tool alarms, fault detection
- Support R&D projects
- Contribute to yield enhancement and process industrialization

### Profile

Both junior and senior profiles will be evaluated, with the following background and characteristics:

- PhD or Master in scientific faculty, e.g. in Engineering, Material Science, Physics
- Experience in silicon or SiC processing and flow knowledge is a plus
- Good manual skills, precision and attention to details
- Fluent in English language is a must
- Good communicator, able to create good relationships and working harmoniously with people
- Committed, persistent and curious

The Employment search is addressed to candidates of both genders, under Law 10.04.1991 n. 125, as amended by Legislative Decree n. 198/2006 which guarantees gender equality at work

## Candidate criteria

Education level required 5 - Master degree  
Experience level required 2-5 years  
Languages English (2- Business fluent)

### Requesting party and reason for requisition

Desired start date 01/05/2023