

2023-31049 Digital Verification Engineer M/F

General information

Job level	30 - Graduate Entry Level
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Position description

Regions	Europe
Country	Italy
States	Catania
Posting title	Digital Verification Engineer M/F
Regular/Temporary	Regular

About MDG

Microcontrollers & Digital ICs Group (MDG) contributes to many of ST's strategic goals for Industrial, Personal Electronics and Communications Equipment. MDG represents about one third of ST's business. MDG is the world's leading manufacturer of microcontrollers and a leader in embedded SIM cards and EEPROM. It's focuses on the market for contactless NFC products and RF mixed signal technologies.

MDG represents around 3 000 employees (mainly R&D teams in France) located in 40 countries, with a revenue of above 3 billion dollars in 2020. It is addressing 2 major activities: the General-Purpose Microcontroller subgroup, the Memory Division, and the Secure Microcontroller Division, which covers products based on non-volatile memory technology and accounts for about 80% of MDG's revenue. Then, the Radio Frequency and Communication sub-group, which focuses on RF, Digital & Mixed Signals activities, representing about 20% of MDG activities.

About GPM

The General-Purpose Microcontroller Sub-Group (GPM) is the largest division at ST Microelectronics and the world leader in the microcontroller market supported by our STM32 products.

Leader in the Internet of Things market which is a rapidly expanding sector driven by strong growth, GPM Division asserts its ambition in the Microcontroller market (STM32) by continuing to innovate and to bring tomorrow's solutions.

Passionated, motivated, join our teams of state of the art engineers.

About Design Verification Team

You will join a dynamic, collaborative, and creative team operating at the bleeding edge between Hardware and Software domains.

You will work on the digital verification of STM32 Digital IPs, and specifically on:

- The verification strategy definition and the verification plan, based on the Digital IP specification and microarchitecture and including the coverage model;
- The verification environment development, the test suite running, debugging and bug reporting;
- The coverage analysis and project closure, including the final verification document and the project review.

Our state of the art methodologies and tools grant the quality of our STM32 products and will allow you to contribute to its worldwide success.

Our multicultural environment (Italy, France, Tunisia and India) and its diversity are the strength of our dynamic team. A structured development plan will bring you quickly hands-on-the-job and evolve with you along all your career.

We are looking for people with enthusiasm, constantly curious and creative towards continuous innovation, with good communication skills and a "detective" attitude.

The future is verified by us, join us!

Profile

Technical competences include:

- Constrained-driven verification, uVM methodology and System Verilog Language;
- Formal verification methodologies and SVA;
- Python and TCL/TK scripting languages, object oriented programming languages;
- HDL (VHDL/Verilog) languages;
- Main EDA verification tools platforms for both simulation and formal verification;
- STM32 Microcontroller architecture

The Employment search is addressed to candidates of both genders, under Law 10.04.1991 n. 125, as amended by Legislative Decree n. 198/2006 which guarantees gender equality at work

Candidate criteria

Education level required	5 - Master degree
Experience level required	2-5 years
Languages	English (2- Business fluent)

Requesting party and reason for requisition

Desired start date	01/03/2023
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