2023-30962 Analog Design FE M/F

General information

Job level 30 - Graduate Entry Level

Position description

Regions	
Country	Italy
States	Catania
	Analog Design FE M/F
Regular/Temporary Job description	Regular
	The role is aimed at supporting the product development for Power Management solution.
	The requested figure is represented by brilliant designer able to propose ideas and design solutions in the context of switching DC-DC converters and LDO.
	The activities will be focused on architectural study and definition, transistor-level design, simulation and verification to guarantee the customer specification. The candidate will work also closely to the layout engineer to drive the layout activity of the designed blocks and he will verify the layout impact with post-layout simulations. He will participate to the evaluation and characterization of the assigned blocks to verify the functionality and the performances.
	Technical skills and communicative attitude are key ingredients to work in a team from the product specification to the product industrialization.
	Good knowledge of analog design. Knowledge of switching regulators architectures is welcome as well as experience in the use of tools like Simulink & Matlab & Simplis for modeling activities.
	Basic knowledge of silicon process technologies
	Attitude to team working, pro-activeness and open mind behavior.
	The Employment search is addressed to candidates of both genders, under Law 10.04.1991 n. 125, as amended by Legislative Decree n. 198/2006 which guarantees gender equality at work.
Profile	
	Basic knowledge in design tool: Cadence virtuoso - Spectre, Mentor AMS are preferred.
	Know-how on Basic programming skills (VHDL_AMS, Verilog, Matlab,) could complete the candidate profile. SV-RNM knowledge is a plus.

Candidate criteria

Education level required	5 - Master degree
Experience level required	Less than 2 years
Languages	English (2- Business fluent)

Requesting party and reason for requisition

Desired start date 06/03/2023